

FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Rev. 2-32) PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO.	SERIAL NO.
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		1054.022 APPLICANT	Not yet assigned
(Use several sheets if necessary)		Phillip Johnson, Gary Powell and William Andrews FILING DATE	GROUP
		Not yet assigned	2817 Not yet assigned
U.S. PATENT AND TRADEMARK OFFICE			

OTHER DOCUMENTS (including Author, Title, Date, Printed Pages, etc.)		
PNBS	AA	"CMOS DLL Based 2V, 2.3ps Jitter, 1GHz Clock Synthesizer and Temperature-Compensated Tunable Oscillator," by David J. Foley, <i>Student Member, IEEE</i> , and Michael P. Flynn, <i>Senior Member, IEEE</i> . <i>IEEE Journal of Solid-state Circuits</i> , Vol 36, Issue 3 March 2001 pp 417-423

EXAMINER SHINNILETON DATE CONSIDERED 12/17/04

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.